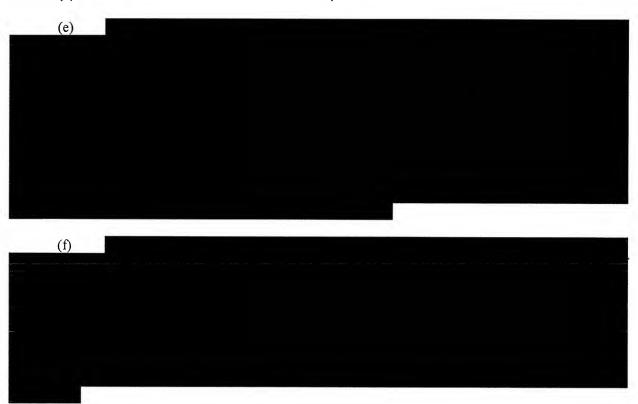
			Approved for	PTO/SB/96 (10-08) use through 11/30/2008. OMB 0651-0031				
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.								
STATEMENT UNDER 37 CFR 3.73(b)								
Applicant/Patent O	Applicant/Patent Owner: Aptina Imaging Corporation							
Application No./Pa	tent No.:	7,183,531	Filed/Issue Date:	February 27, 2007				
Entitled: AMPLIFICATION WITH FEEDBACK CAPACITANCE FOR PHOTODETECTOR SIGNALS								
Aptina (Name of Assignee)	Imaging Corporatio	n , a (Type	Corpo e of Assignee, e.g., corporation, partne	oration ership, university, government agency, etc.)				
states that it is:								
1. x the ass	ignee of the entire rig	ht, title, and interest;	or					
2. an assi	gnee of less than the	entire right, title and	interest.					
(The ex	tent (by percentage)	of its ownership inter	est is %)					
in the patent applic	cation/patent identified	d above by virtue of e	either:					
A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel								
Frame _	, or	for which a copy ther	eof is attached.					
	title from the inventor	(a) of the notest applie	action/notant identified above	to the current assignee as follows:				
B. X A chain of								
From: Alf Olsen et al.     To: Micron Technology, Inc.     The document was recorded in the United States Patent and Trademark Office at								
Ree			, or for which a copy					
2. From:	Micron Technol	ogy, Inc.	To: Aptina Imagir	ng Corporation				
The		····	States Patent and Tradem					
Ree	el	Frame	, or for which a copy	thereof is attached.				
3. From:			To:					
The	document was reco	orded in the United	States Patent and Tradema	ark Office at				
Ree	əl,	Frame	, or for which a copy t	thereof is attached.				
Addit	ional documents in t	he chain of title are	listed on a supplemental s	heet.				
X As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.								
[NOTE: A s Assignment <u>See</u> MPEP	Division in accordance	true copy of the origin ce with 37 CFR Part	nal assignment document(s) 3, to record the assignment	) must be submitted to in the records of the USPTO.				
The undersigned (whose title is supplied helow) is authorized to act on behalf of the assignee.								
	MU	X	Ν	ovember 25, 2008				
***	Signature			Date				
	Gianni Minu	utoli		(202) 420-3191				
	Printed or Typed			Telephone Number				
	Attorney for As	signee						
	Title							

#### PATENT ASSIGNMENT AGREEMENT

### 1. Definitions

- (a) "Agreement" shall mean this Patent Assignment Agreement.
- (b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.
- (c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.
  - (d) "Effective Date" shall mean October 3, 2008.



- (g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

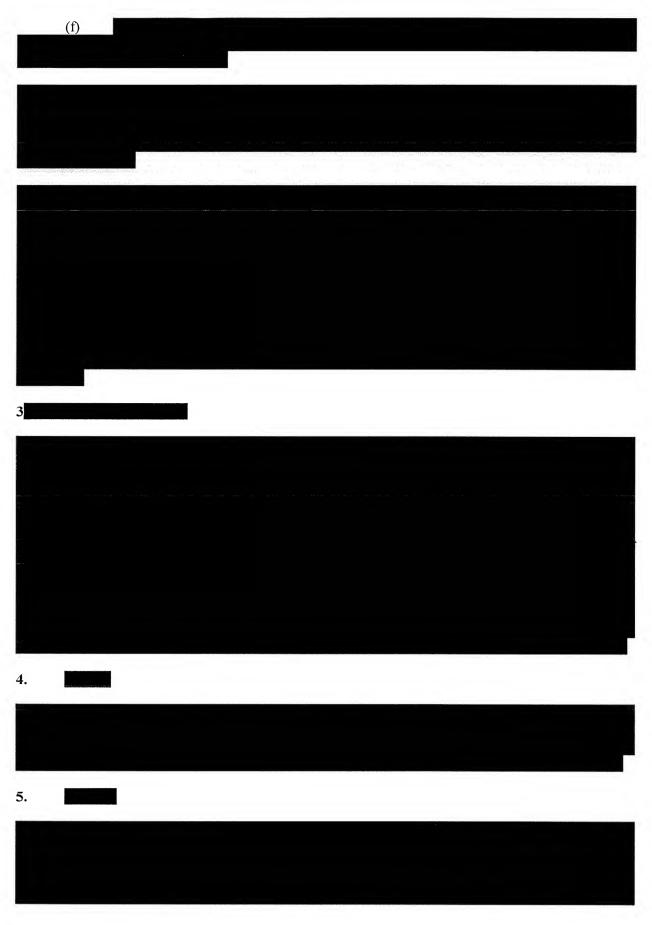


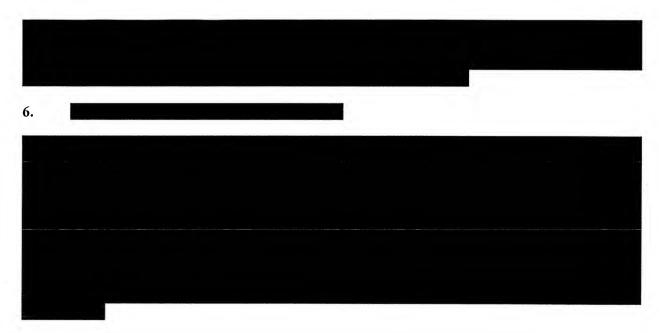
- (a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.
- (b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.



- (d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.
- (e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any,

  Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.





### 7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

[the rest of this page intentionally left blank]

	Micron Technology, Inc.
	Signed:
	Date: 3/20/ss , 2008
Notarization State of Idaho County of Ada	Cl n A U
On 126/08, before me, Parla W. Dang	_, personally appeared the above-named Skin RAppleta
	nowledged to me that he did so of his own free will and in his  Signed:
REVIEWED MTI LEGAL	My commission expires: 8/12/11
C. Summing R. S.	Agreed to by:
TARLING TARLING TO THE TARLING THE OF	Aptina Imaging Corporation  Signed: Jour J. Jawr J  Date: 9/26/09 , 2008
Notarization State of Idaho County of Ada	
On 9/26/08, before me, Paula R. Spans (notary name)	, personally appeared the above-named Thomas Llaus, JV.
authorized capacity for the purposes set forth herein.	nowledged to me that he did so of his own free will and in his
A R. S. A.	My commission expires: 8/12/11
A R. S STATE OF DATE O	
The state of the s	

Agreed to by:

[Signature page to the Patent Assignment Agreement]

## ATTACHMENT "A"

### **IMAGING PATENTS**

# Aptina Issued

PATENT NUMBER	FILE NUMBER 100 100 100 100 100 100 100 100 100 10	TITLE # SEE TO SEE THE	COUNTRY NAME	ISSUE DATE
		FORMATION		
7157683	#2003-0824.00/US	METHOD, APPARATUS AND SYSTEM PROVIDING CONFIGURABLE CURRENT SOURCE FOR IMAGE SENSORS	United States of America	Jan 2, 2007
7164161	2003-0331.00/US	DUAL GATE STRUCTURE FOR IMAGERS AND METHOD OF FORMATION	United States of America	Jan 16, 2007
7166879	1997-1466.01/US	PHOTOGATE WITH IMPROVED SHORT WAVELENGTH RESPONSE FOR A CMOS IMAGER	United States of America	Jan 23, 2007
7170117	2003-0105.01/US	IMAGE SENSOR WITH IMPROVED DYNAMIC RANGE AND METHOD OF FORMATION	United States of America	Jan 30, 2007
7183531	PBIT-0154.00/US	AMPLIFICATION WITH FEEDBACK CAPACITANCE FOR PHOTODETECTOR SIGNALS	United States of America	Feb 27, 2007
7184284	2003-1218.00/US	CLOSED-LOOP HIGH VOLTAGE BOOSTER	United States of America	Feb 27, 2007
7186964	2002-0308.01/US	DARK CURRENT REDUCTION: CIRCUITRY FOR CMOS ACTIVE PIXEL SENSORS	United States of America	Mar 6, 2007
7188036	2003-0067.01/US	INTERNAL BIAS MEASURE WITH ONBOARD ADC FOR ELECTRONIC DEVICES	United States of America	Mar 6, 2007
7189954	2003-0917.00/US	MICROELECTRONIC IMAGERS WITH OPTICAL DEVICES AND, METHODS OF MANUFACTURING SUCH MICROELECTRONIC IMAGERS	United States of America	Mar 13, 2007
7190039	2004-0187.00/US	MICROELECTRONIC IMAGERS WITH SHAPED IMAGE SENSORS AND METHODS FOR MANUFACTURING MICROELECTRONIC IMAGERS	United States of America	Mar 13, 2007
7196829	AVGO-0044,00/US	DIGITAL IMAGE SYSTEM AND METHOD FOR COMBINING SENSING AND IMAGE PROCESSING ON SENSOR WITH TWO-COLOR PHOTO-DETECTOR	United States of America	Mar 27, 2007
7199349	PBIT-0154.01/US	AMPLIFICATION WITH FEEDBACK CAPACITANCE FOR PHOTODETECTOR SIGNALS	United States of America	Apr 3, 2007